Exam 1 Topics
Exam Date: Friday, February 23, 2007

Topics and potential questions that you can expect on Exam #1 include:

(1) Binary numbers.
(2) Binary-to-Decimal conversion and Decimal-to-Binary conversion.
(3) Octal and Hexadecimal numbers.
(4) 2’s Complement numbers.
(5) Binary Coded Decimal (BCD).
(6) Binary addition and subtraction.
(7) Understanding of fundamental Boolean operations (NOT, AND, OR, NAND, NOR, XOR, XNOR).
(8) Simplification of Boolean expressions using Boolean algebra postulates and theorems.
(9) Deriving a Sum-of-Minterms or Product-of-Maxterms expression from a Boolean function table.
(10) Deriving a Sum-of-Products or Product-of-Sums expression from a K-map.
(11) Wiring and functionality of circuits constructed in labs 1-4. IC schematics will be given.

Notes:
(1) The use of calculators and electronic devices of any kind will not be permitted on the exam.
(2) Seating will be randomly assigned.
(3) The exam is closed book, closed notes.