Exam 3 Topics – Exam will be Wednesday. May 1

Topics

You can expect the following topics and types of questions to be on the exam:

- Parallel Load Registers
- Shift Registers (Including Universal Shift Registers)
- Serial Addition
- Synchronous Binary Counters
- Arbitrary Count Counters (with and without unused states)
- Ring Counters, Johnson Counters, and their variations
- RAM and ROM memory – How do we read and/or write
- Address and Data structure – Address lines, Word size, Address decoding
- Error Correction and Hamming Codes.
- Combinational Circuit Implementation with ROMs.