Problem 1:
1. Using Figure 1, write KVL equation for the closed loop as shown.
2. Using Figure 2, write a KCL equation at the node shown with a black dot.

-\( -V_{IS} + V_I + V_{R3} = 0 \)
-\( i_{R3} + i_{I} - i_{R4} + i_{VS} = 0 \)

Problem 3:
1. Using the passive sign convention, mark current directions and signs on the resistors and current source and the direction of current flow through the voltage source.
2. Identify all nodes (trivial and non-trivial)
3. Identify all meshes.